



# TIM-CJ, TIM-CL, TIM-CS

## GPS Receiver Board

### SiRFstar™ II Positioning Engine

The TIM-CJ, TIM-CL and TIM-CS are fully self-contained OEM GPS receiver boards powered by TIM which are based on the SiRFstar™ II GPS technology. These boards are available in different form factors, comparable to those of popular GPS receivers in the market. The integrated data logger allows storing GPS data on the receiver.

#### TIM-CJ



**Compatible to:**  
Conexant / Navman  
Jupiter

**Supply voltage:**  
3.3 or 5V

**Size:** 71 x 41 x 11 mm

#### TIM-CL



**Compatible to:**  
Trimble Lassen LP

**Supply voltage:** 3.3V

**Size:** 66 x 32 x 10 mm

#### TIM-CS



**Compatible to:**  
Trimble  
Lassen SKII, SK8

**Supply voltage:** 5V

**Size:** 82 x 32 x 12 mm

#### Overview

These GPS receiver boards provide complete GPS signal processing from antenna input to serial data output (NMEA or binary format).

Advanced power-saving modes allow an additional reduction of power consumption for applications where power consumption is of primary concern. The receiver board featuring the GRF2i RF front-end chip and an integrated Low-Noise Amplifier (LNA) connects seamlessly to low-cost passive antennas as well as active antennas.

They provide two serial ports, which can handle NMEA or SiRF® proprietary data format or accept differential GPS data (RTCM). The integrated data logging function stores up to 100'000 logs on the internal FLASH memory.

#### Benefits

- Fully self-contained GPS receiver (PVT output)
- Low power consumption
- Excellent GPS performance
  - Fast time-to-first-fix
- Emulates common form factors to enable easy transition to u-blox technology
  - Mechanical dimensions
  - Electric and signal interfacing
- Supports Satellite Based Augmentation Systems (SBAS)
- Integrated data logging
- Fully EMI shielded
- Passive and active antenna support
- Active antenna supervisor for short and open circuit detection
- Immune to RF interference

#### Features

- 12 channel GPS receiver
- 1 Hz position update rate
- SiRFstar™ II architecture
  - GRF2i RF front-end IC
  - GSP2e GPS DSP with integrated Real Time Clock
- 8 MBit FLASH memory
- Low noise amplifier
- Power management features
  - TricklePower™ mode
  - Push-to-Fix™ mode
- Battery voltage supply pin for internal backup memory and real time clock
- Industrial operating temperature range -40...85°C

#### Support Products

##### TIM Evaluation Kit

Use the TIM Evaluation Kit to experience the power of u-blox GPS technology inside the u-blox GPS receiver boards.

*your position  
is our focus*



