

Data Sheet

LP10A/LP25A

Digital Logic Probes



Specialty handheld probes

The LP series of logic probes troubleshoot and analyze most digital logic circuits.

LP10A: For most types of TTL and CMOS circuits

- Detects pulses > 30 ns up to 20 MHz, TTL, CMOS levels
- Dual LEDs indicate hi/lo state

LP25A: Tests more types of digital circuits

- Detects pulses > 30 ns up to 30 MHz, DTL, RTL, NMOS, MOS Logic
- Dual LEDs and tone indicate hi/lo state

Specifications

Function	LP10A	LP25A
TTL/CMOS Selectable	yes	yes
Signal	Requirements	Levels
	Logic "1"	TTL: > 2.3 V ± 0.2 V DC CMOS: > 70 % V cc ± 10%
	Logic "0"	TTL: < 0.8 V ± 0.2 V DC CMOS: < 30 % V cc ± 10%
Maximum Input Signal Frequency	20 MHz	
Minimum Detectable Pulse Width @ + 3 V	Frequency	Pulse Width
	< 1 kHz	100 ns
	1 kHz to 20 kHz	50 ns
	20 kHz to 20 MHz	30 ns
Input Impedance	1 MΩ	1 MΩ
Pulse Indicator Flash Time	500 mS	500 mS
Audible Tone at Pulse Rate	no	yes
Pulse/Memory	Pulse uses the pulse detection modes listed above. Memory allows the probe to capture a positive or negative pulse greater than 30 nS in width. The yellow LED will come on and stay ON until switch is reset to PULSE.	
Power Requirements (from circuit under test)	LP10A	LP25A
Power Supply	5 to 15 V dc	5 to 15 V dc
Power Supply Current @ 5 V	30 mA	40 mA
@ 15 V	80 mA	90 mA
Power Supply Protection	20 V ac/V dc	20 V ac/V dc
Maximum Input Voltage (15 seconds)	40 V ac/V dc	40 V ac/V dc

General Specifications

Operating Temperature	0 °C to 50 °C at 80 % R.H.
Storage Temperature	- 20 °C to 65 °C at 75 % R.H.
Dimensions	210 mm x 18 mm x 18 mm (8.2 in x 0.7 in x 0.7 in)
Weight	LP10A: 38.1 g (1.3 oz), LP25A: 42.6 g (1.5 oz)

No waiting.

No shipping charges.

No hassle warranty



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